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INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT

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Application Number	10/026,312
Filing Date	12/21/2001
First Named Inventor	Matthew Aubury
Art Unit	2123
Examiner Name	L. Osborn & Fred Ferris III
Attorney Docket Number	Technology Center 2100 44359/08329 EMBIP074

## NON PATENT LITERATURE DOCUMENTS

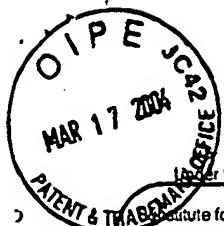
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
VG		PARKINSON M F ET AL: "Profiling in the ASP codesign environment" SYSTEM SYNTHESIS, 1995., PROCEEDINGS OF THE EIGHTH INTERNATIONAL SYMPOSIUM ON CANNES, FRANCE 13-15 SEPT. 1995, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 13 September 1995 (1995-09-13), pages 128-133, XP010192178. ISBN: 0-8186-7076-2 the whole document	
VG		HARKIN J ET AL: "Partitioning methodology for dynamically reconfigurable embedded systems" IEE PROCEEDINGS E. COMPUTERS & DIGITAL TECHNIQUES, INSTITUTION OF ELECTRICAL ENGINEERS. STEVENAGE, GB, vol. 147, no. 6, 28 November 2000 (2000-11-28) pages 391-396, XP006013943 ISSN: 1350-2387 the whole document.	
VG		TAMMEMAE K ET AL: "AKKA: A Tool-kit for Cosynthesis and Prototyping" IEE DIGEST: COLLOQUIUM ON HARDWARE-SOFTWARE COSYNTHESIS FOR RECONFIGURABLE SYSTEMS, vol. 96, no. 036 February 1996 (1996-02), pages 8/1-8/8, XP002244550 the whole document	

Examiner Signature		Date Considered	7/12/05
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 509. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Application Number	10/026,312
Filing Date	12/21/2001
First Named Inventor	Matthew Aubury
Art Unit	2128-123
Examiner Name	L. Osborne Fred Ferris
Attorney Docket Number	Technology Center 2100 44359/08329 EMB1P074

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
<i>MAS</i>		GREHAN R: "Code Profilers: Choosing a Tool for Analyzing Performance" METROWERKS WHITE PAPERS, 'Online' 28 April 1999 (1999-04-28). XP002244551 Retrieved from the Internet: [URL:www.metrowerks.com/pdf/CodeProfilers.pdf] 'retrieved on 2003-06-17! page 2 page 4 - page 6	
<i>AD</i>		WO D ET AL: "Compiling to the gate level for a reconfigurable co-processor" FPGAS FOR CUSTOM COMPUTING MACHINES, 1994. PROCEEDINGS, IEEE WORKSHOP ON NAPA VALLEY, CA, USA 10-13	
		APRIL 1994, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, 10 April 1994 (1994-04-10), pages 147-154, XP010098110 ISBN: 0-8186-5490-2 the whole document.	
<i>b5</i>		FRUMKIN M ET AL: "Trace-driven debugging of message passing programs" PARALLEL PROCESSING SYMPOSIUM, 1998. IPPS/SPDP 1998. PROCEEDINGS OF THE FIRST MERGED INTERNATIONAL... AND SYMPOSIUM ON PARALLEL AND DISTRIBUTED PROCESSING 1998. ORLANDO, FL, USA 30 MARCH-3 APRIL 1998, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 30 March 1998 (1998-03-30), pages 753-762.	
		XP010276666 ISBN: 0-8186-8404-6 the whole document	

Examiner Signature	<i>L. Osborne</i>	Date Considered	7/22/05
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